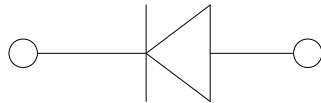


- High efficiency
- High current capability
- High reliability
- High surge current capability
- Low power loss
- Glass passivated chip junction
- Solder dip 275 °C max. 7 s, per JESD 22-B106



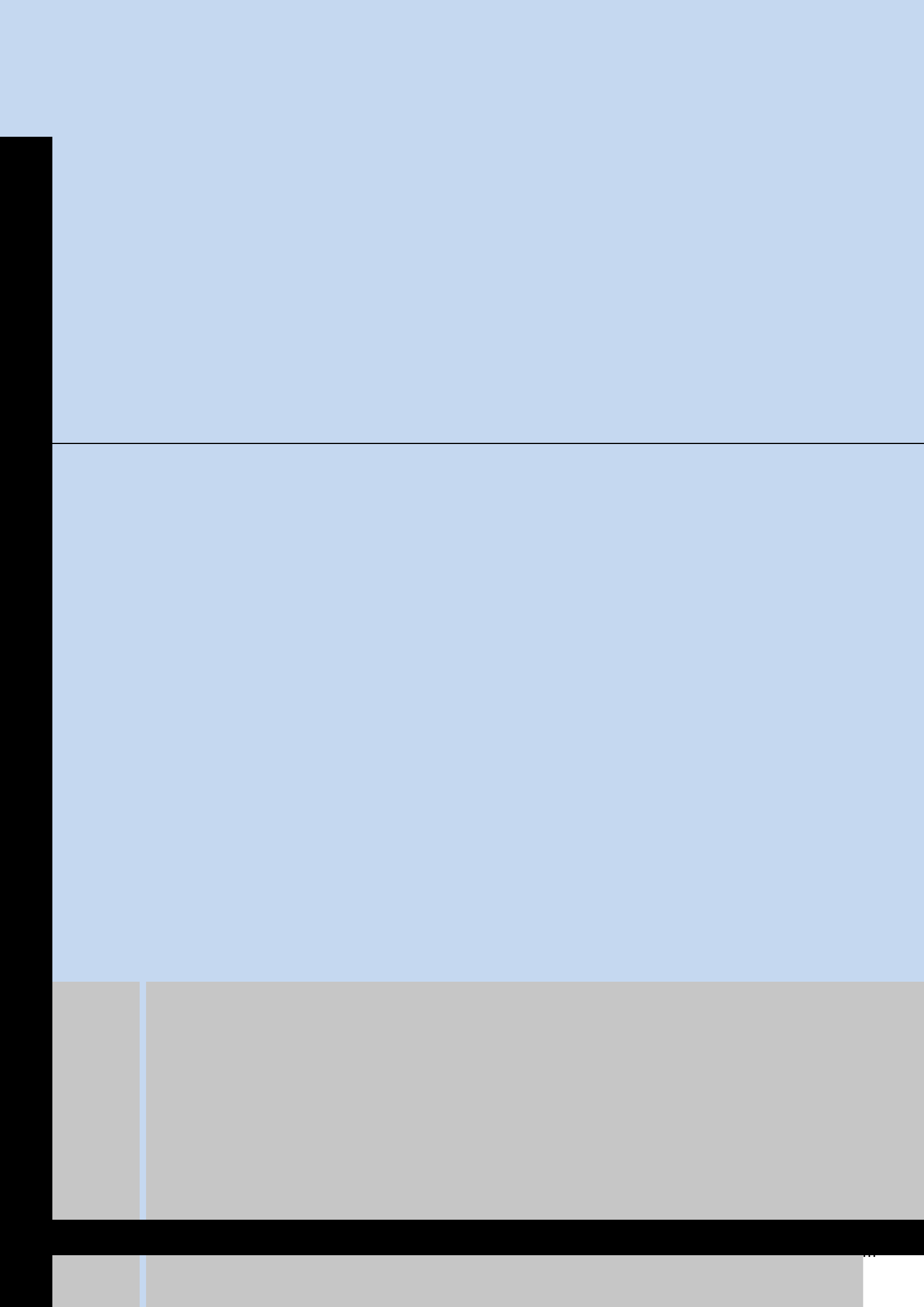
- : R-1
Molding compound meets UL 94 V-0 flammability rating,
RoHS-compliant, Halogen free
- : Tin plated leads, solderable per J-STD-002 and JESD22-B.1
M

Device marking code			1F1G	1F2G	1F3G	1F4G	1F5G	1F6G	1F7G
Maximum Repetitive Peak Reverse Voltage	VRRM	V	50	100	200	400	600	800	1000
Maximum RMS Voltage	VRMS	V	35	70	140	280	420	560	700
Maximum DC blocking Voltage	VDC	V	50	100	200	400	600	800	1000
Average Forward Current @60Hz sine wave, Resistance load, Ta=70	I _{F(AV)}	A	1.0						
Forward Surge Current (Non-repetitive) @60Hz Half-sine wave,1 cycle, Tj=25	I _{FSM}	A	30						
Forward Surge Current (Non-repetitive) @1ms, square wave, 1 cycle, Tj=25			60						
Current squared time @1ms≤t≤8.3ms Tj=25 Rating of per diode	I ² t	A ² s	3.375						
Typical junction capacitance @Measured at 1MHz and Applied Reverse Voltage of 4.0 V.D.C	Cj	pF	10					8	
Storage Temperature	T _{stg}		-55 ~ +150						
Junction Temperature	T _j		-55 ~ +150						

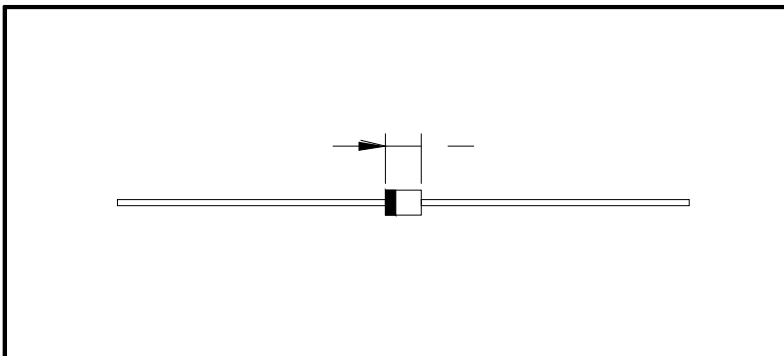
■ Ta=25 Unless otherwise specified



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■



Dim	Min	Max
A	3.00	3.30
B	2.00	2.70
C	25.4	/
D	0.53	0.64



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